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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

P07156US00

In re patent application of
Saïd EL FASSI et al.

New U.S. Patent Appln. Corr. to
French Patent Appln. No.: 99 12055

Serial No.: 09/822,820
Filed: April 2, 2001

Assignee: MATRA TRANSPORT INTERNATIONAL

For: A SECURE COMPUTER SYSTEM

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INFORMATION DISCLOSURE STATEMENT

To Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

S I R:

Pursuant to C.F.R. 1.97-1.99 the Applicants hereby submit the attached:

- (a) list of prior art which has been cited in the Search Reports prepared by the European Patent Office with respect to the corresponding French Patent Application and European Patent Application;
- (b) a copy of the Search Report, which is attached for the U.S. Examiner to know the first opinion of the Examiner of the E.P.O. and the portions of the references found pertinent by the latter;
- (c) a copy of the references cited under (a).

Please find herewith the U.S. counterpart No. 5,794,167 of European

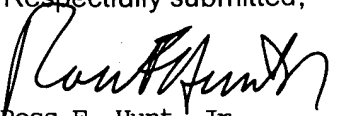
Patent 0,621,521.

As regards the prior art which is not in English, the Search Report comprises indications which constitute the required "concise statement of relevance".

Consideration and citation of the references are respectfully solicited.

July 10, 2001

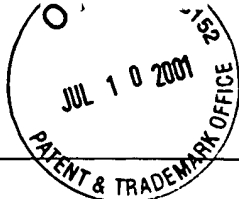
Respectfully submitted,


Ross F. Hunt, Jr.
Reg. No. 24,082

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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		ATTY DOCKET No. P07156US00		SERIAL NO. 09/822,820			
		APPLICANT : Saïd EL FASSI et al.					
		FILING DATE April 4, 2001		GROUP Unassigned			
U.S. PATENT DOCUMENTS							
Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
		5,794,167	08/11/98	Gruere et al.			
		(US counterpart of EP 0621521)					
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION Yes No
		2,169,114	July 2, 1986	Great Britain			
		0,621,521	Oct. 26, 1994	Europe			
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
		"Offline Residue Checking for a Floating-Point Arithmetic Unit Executing overlapped Operations" IBM Technical Disclosure Bulletin, U.S., IBM Corp. New York, Vol. 30, No. 6, Nov. 1, 1987 (1987-11-01), Pages 391-392, XP000023332 - ISSN: 0018-8689 - Whole document.					
		Mahmood A. et al.: "Concurrent Error Detection Using Watchdog Processors - A Survey" IEEE Transactions on Computers, U.S., IEEE Inc. New York, Vol. 37, No. 2, February 1, 1988 (1988- 02-01), pages 160-174, XP000648398 - ISSN: 0018-9340 - Page 160, left column, line 34 - right column, line 22.					
EXAMINER				DATE CONSIDERED			
* EXAMINER : Initial if reference considered, whether or not citation is in conformance with MPEP 609 ; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Form PTO-FB-A820